

Parallel Port Register Read/Write Tool

Help user to make sure the platform whether able to access legacy ISA address
278/378/3BC and standard (resource assigned by system automatically)

Test Tool Simple User Guide

First Edition, December 2015



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Table of Contents

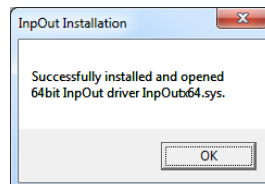
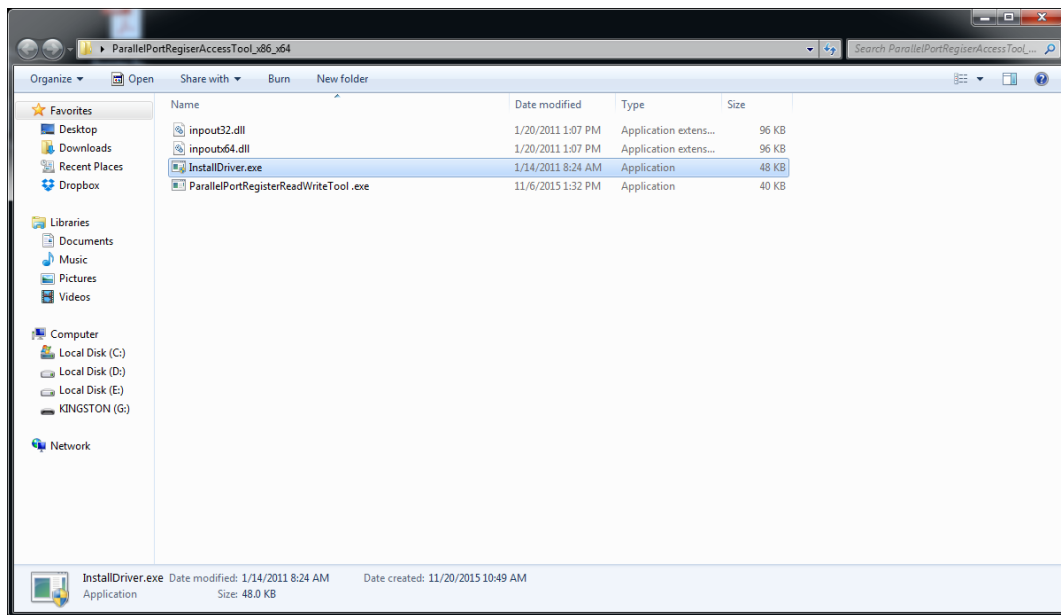
1. Tool Feature	3
2. Preparation Before Execute Tool	3
3. Set IO Address	5
3.1 278h.....	5
3.2 378h.....	5
3.3 3BCh.....	5
3.4 Resource Assigned By System Automatically.....	6
3.5 Select Parallel Port IO Address.....	6
4. Register	7
4.1 Register Table.....	7
4.2 Select Register.....	7
5. Read/Write	8
5.1 Use ECR Register To Make An Example.....	8

1. Tool Feature

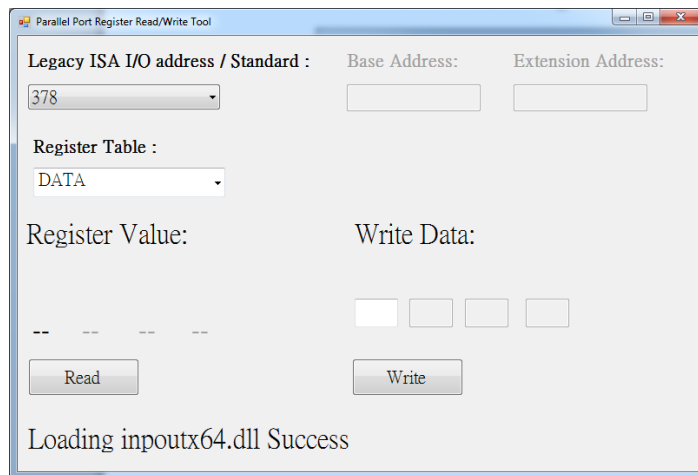
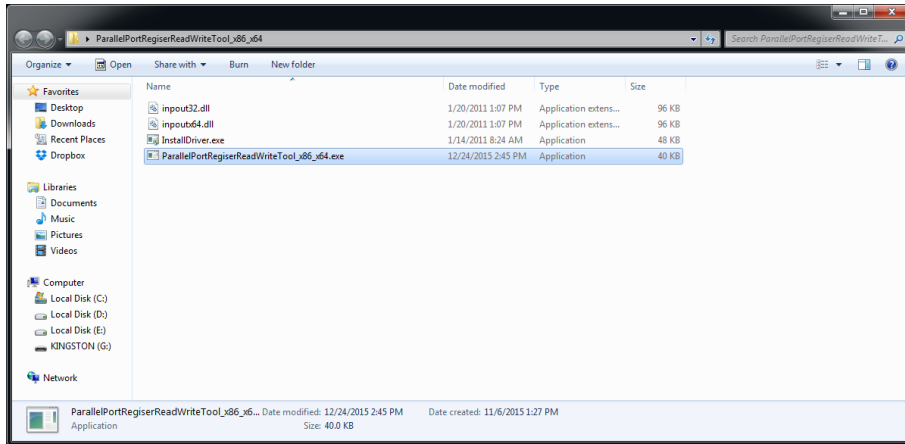
Parallel Port Register Read/Write Tool could verify the IO address which mapped with the parallel port, If the test result is unable to access the register, The indication is the system doesn't assigned the IO address or the IO address is conflicting, Legacy ISA Address especially (278h/378h/3BCh), some motherboard BIOS prohibit **PAR5008R** Legacy ISA Address remap function, this situation cause users couldn't use some parallel port device which binding with Legacy ISA Address(ex. Keypro),the tool could help user make sure the platform which whether allow Legacy ISA Address remap function.

2. Preparation Before Execute Tool

- a. Please access to SUNIX website to download driver "ParallelPortRegiserReadWriteTool" for PAR5008R.
- b. Run InstallDriver.exe, If x86 machine, System will install InpOut32.sys; If x64 machine, system will install InpOutx64.sys



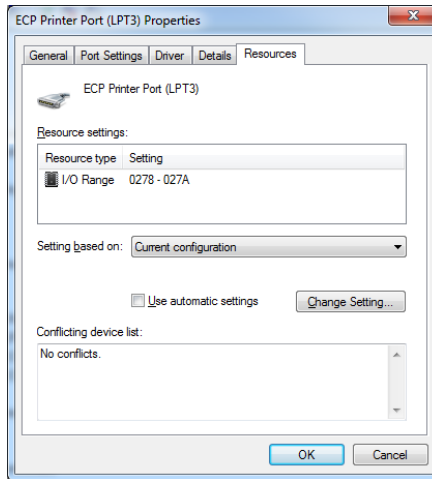
c. Run ParallelPortRegisterReadWriteTool.exe



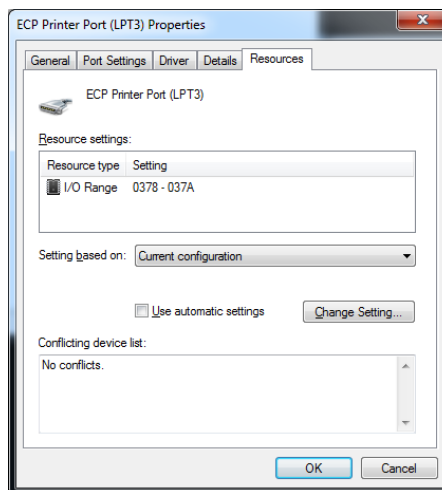
3. Set IO Address

3.1 278h

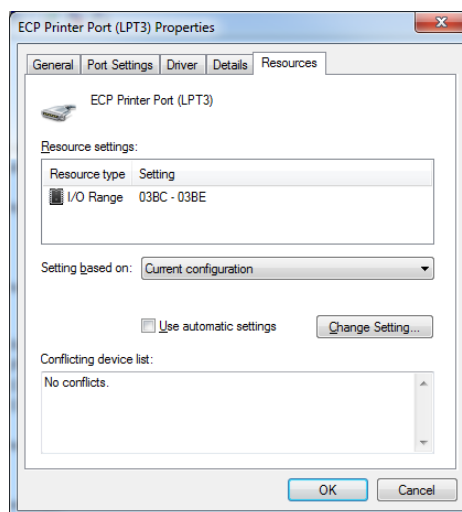
- Check IO address form LPT Properties



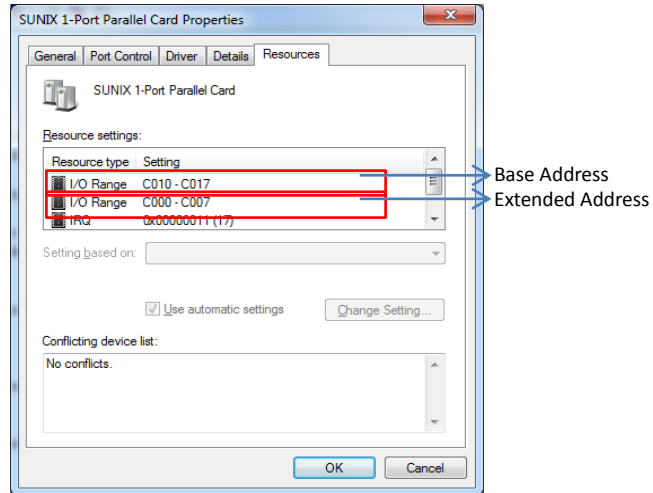
3.2 378h



3.3 3BCh

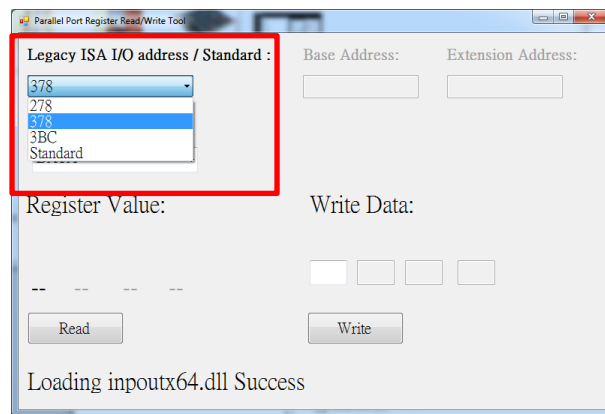


3.4 Resource Assigned By System Automatically

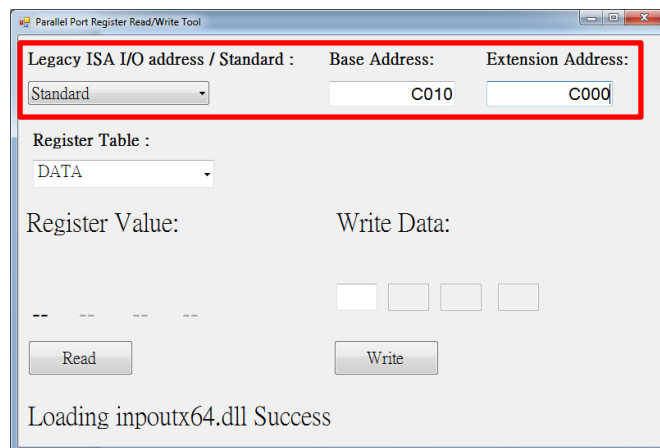


3.5 Select Parallel Port IO Address

- a. Select parallel port IO address in ParallelPortRegisterWriteTool.exe



- b. If select standard , need type base address and extension address, Reference section 2.4

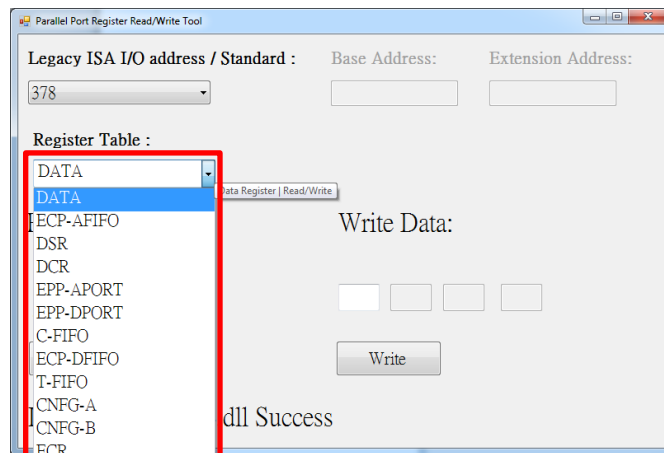


4. Register

4.1 Register Table

Parallel Register	Describe	Read/Write
DATA	Data Register	R/W
ECP-AFIFO	ECP Address FIFO Register	W
DSR	Status Register	R
DCR	Control Register	R/W
EPP-APORT	EPP Auto Address Strobe Register	R/W
EPP-DPORT	EPP Auto Data Strobe Register	R/W
C-FIFO	Parallel Port Data FIFO	W
ECP-DFIFO	ECP Data FIFO Register	R/W
T-FIFO	Test FIFO Register	R/W
CNFG-A	Configuration Register A	R
CNFG-B	Configuration Register B	R
ECR	Extended Control Register	R/W

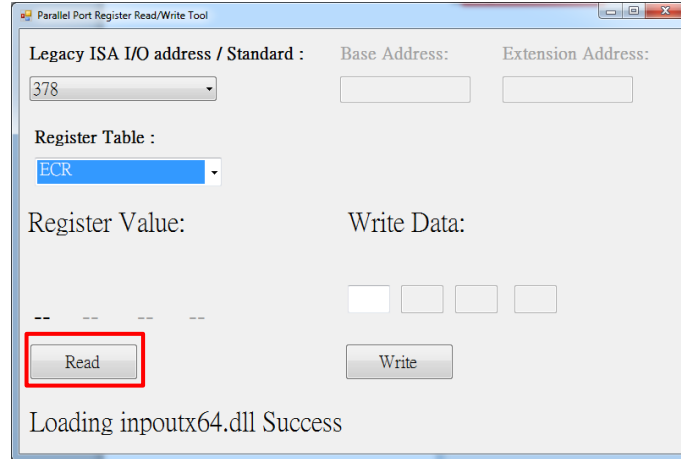
4.2 Select Register



5. Read/Write

5.1 Use ECR Register To Make An Example

a. Read register



Parallel Port Register Read/Write Tool

Legacy ISA I/O address / Standard : 378 Base Address: Extension Address:

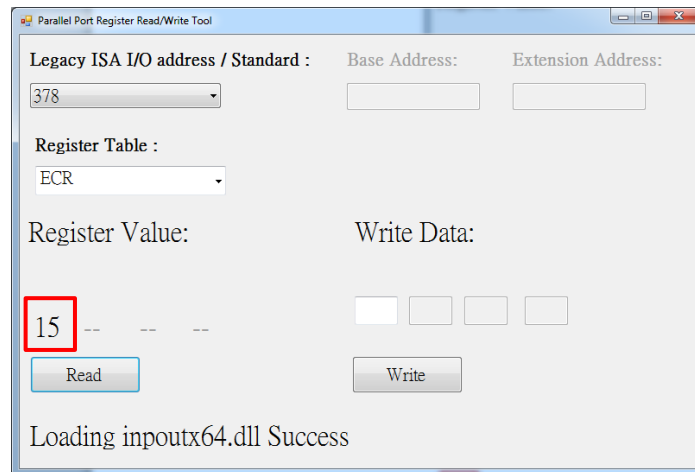
Register Table : ECR

Register Value: Write Data:

Read Write

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0x15(h) = 00010101; Standard Mode(SPP)



Parallel Port Register Read/Write Tool

Legacy ISA I/O address / Standard : 378 Base Address: Extension Address:

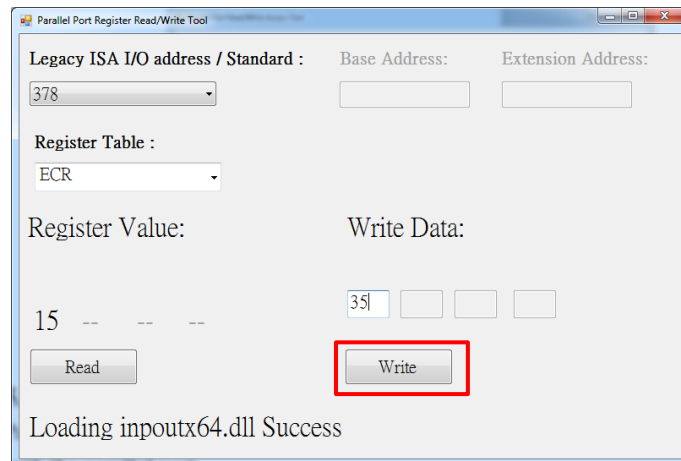
Register Table : ECR

Register Value: 15 Write Data:

Read Write

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b. Write register , set 0x35(h) = 00110101;PS/2 Mode(BPP)



Parallel Port Register Read/Write Tool

Legacy ISA I/O address / Standard : 378 Base Address: Extension Address:

Register Table : ECR

Register Value: 15 Write Data: 35

Read Write

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